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# VIRGINIA STATE BUDGET

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2024 Session

## Budget Bill - HB30 (Introduced)

Bill Order » Part 4: General Provisions » Special Conditions and Restrictions on Expenditures » Item 4-5.08

Semiconductor Manufacturing Performance Grant Programs

### Item 4-5.08

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#### § 4-5.08 SEMICONDUCTOR MANUFACTURING PERFORMANCE GRANT PROGRAMS

a. The Comptroller shall not draw any warrants to issue checks for semiconductor manufacturing performance grant programs, pursuant to Title 59.1, Chapter 22.3, Code of Virginia, without a specific legislative appropriation. The appropriation shall be in accordance with the terms and conditions set forth in a memorandum of understanding between a qualified manufacturer and the Commonwealth. These terms and conditions shall supplement the provisions of the Semiconductor Manufacturing Performance Grant Program, the Semiconductor Memory or Logic Wafer Manufacturing Performance Grant Program, and the Semiconductor Memory or Logic Wafer Manufacturing Performance Grant Program II, as applicable, and shall include but not be limited to the numbers and types of semiconductor wafers that are produced; the level of investment directly related to the building and equipment for manufacturing of wafers or activities ancillary to or supportive of such manufacturer within the eligible locality; and the direct employment related to these programs. To that end, the Secretary of Commerce and Trade shall certify in writing to the Governor and to the Chairmen of the House Appropriations and Senate Finance and Appropriations Committees the extent to which a qualified manufacturer met the terms and conditions. The appropriation shall be made in full or in proportion to a qualified manufacturer's fulfillment of the memorandum of understanding.

b. The Governor shall consult with the House Appropriations and Senate Finance and Appropriations Committees before amending any existing memorandum of understanding. These Committees shall have the opportunity to review any changes prior to their execution by the Commonwealth.